REVISIONS				
REV	ECO	DESCRIPTION	BY/DATE	CHK/DATE
EA		PROTOTYPE RELEASE	MSH/06JUN99	
EB		CHANGE C40,C46 TO 10UF 25V	MSH/5NOV99	
A	10111	PRODUCTION RELEASE	MSH/11NOV99	

NOTES:
(UNLESS OTHERWISE SPECIFIED)

1) ALL THROUGH HOLE RESISTORS EXPRESSED IN OHMS 1/4W 5%
ALL SURFACE MOUNT RESISTORS EXPRESSED IN OHMS 1/10W 5%
2) ALL CAPACITORS EXPRESSED IN MICROFARADS
3) ALL 14 PIN ICS, PIN 14 IS PWR, PIN 7 IS GND.
ALL 16 PIN ICS, PIN 16 IS PWR, PIN 8 IS GND.
ALL 20 PIN ICS, PIN 20 IS PWR, PIN 10 IS GND.
ALL 32 PIN ICS, PIN 32 IS PWR, PIN 16 IS GND.

Jumper Selection
1-2 10 Ohm Termination
2-3 No 10 Ohm Termination

Jumper Selection
1-2 10 Ohm Termination
2-3 No 10 Ohm Termination

Input Connector Pin Functions

1) Channel B attenuation input for 19:1
2) Channel A input to op amp inverting input
3) Channel A input to op amp non-inverting input
4) Channel A output to attenuation resistor
5) Channel A attenuation input for 19:1
6) Channel A attenuation input for 4:1
7) Channel B input to op amp non-inverting input
8) Channel B attenuation input for 4:1
9) Channel B output to attenuation resistor
10) Channel B input to op amp inverting input

Channel A high pass filter

Jumper Selection
1-2 Full bandwidth
2-3 -18dB/Octave < 300 Hz

Channel B high pass filter

Jumper Selection
1-2 Full bandwidth
2-3 -18dB/Octave < 300 Hz

Channel A + B low pass filter

Positive switching regulator

Negative switching regulator

Quad Power Amp

Output Connector Pin Functions

Connector J1
1) Channel A Output A
2) Channel A Output B
3) Channel B Output A
4) Channel B Output B

Connector J5
1) Channel A+B Output A -18dB/Octave >300Hz
2) Channel A+B Output B -18dB/Octave >300Hz

Connector J2
1) Channel A+B Output A Full Bandwidth
2) Channel A+B Output B Full Bandwidth

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INTERNATIONAL GAME TECHNOLOGY
9295 PROTOTYPE DRIVE RENO, NV 89511

TITLE: PCB, IN LINE AMP

DWG. SIZE	DWG. NO.	REV LTR
C	769 245 12	A

DRAWN	DATE	CHECKED	DATE	APPROVED	DATE	SCALE	SHT
M. S. HOY	14OCT99					NONE	1 OF 1

DWG NO: 769 245 12