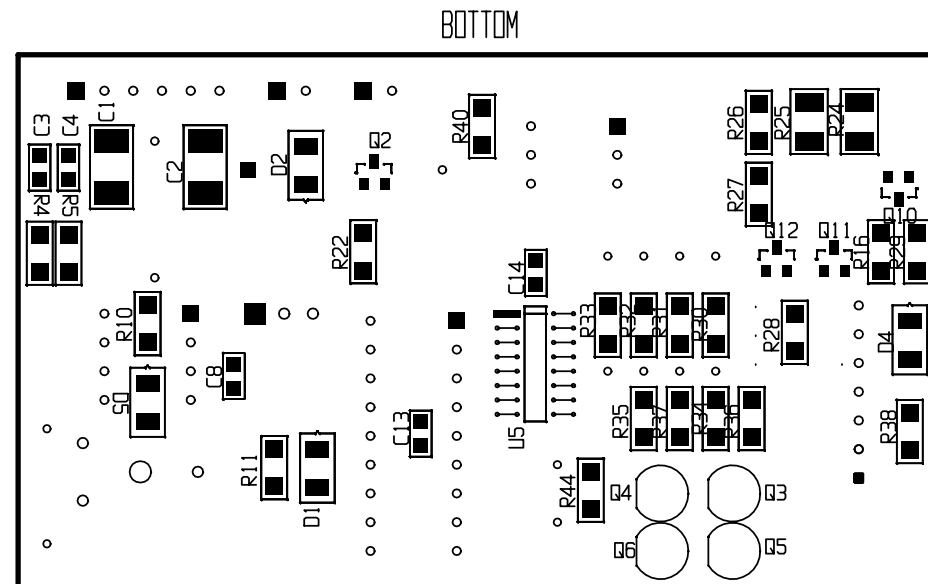
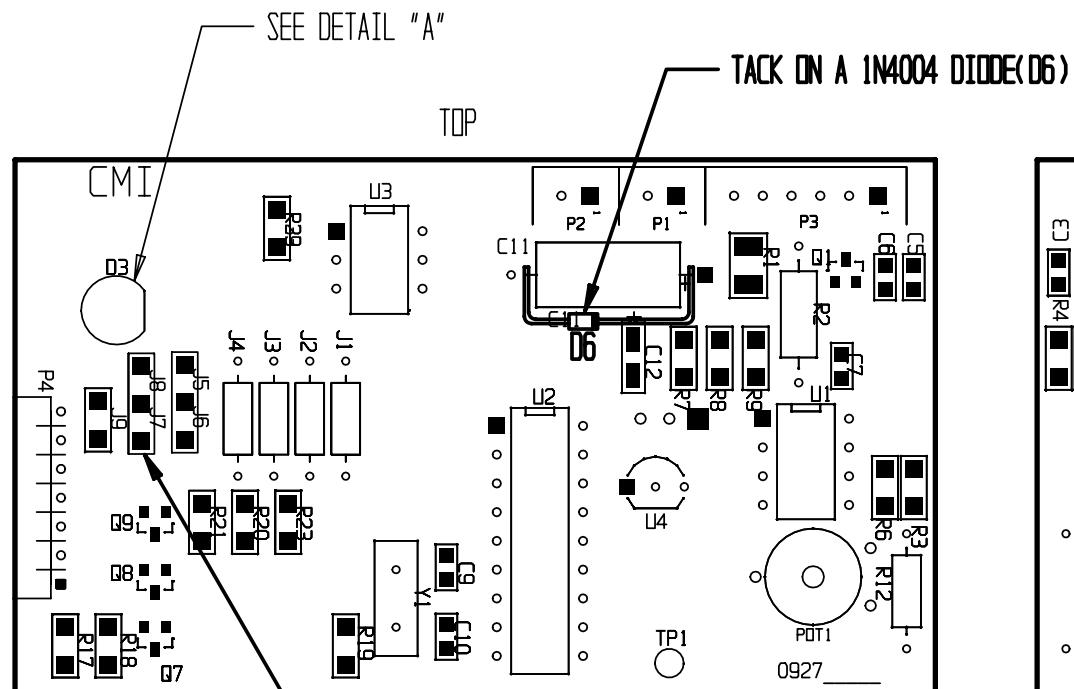
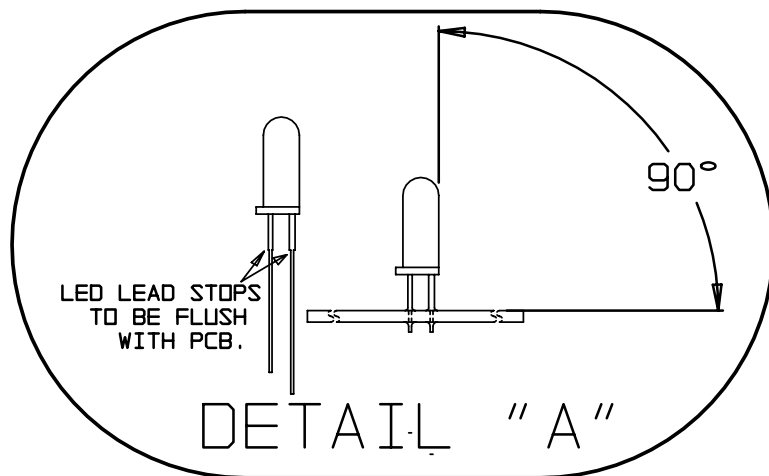


NO.	ECN NO.	REVISIONS	DATE/BY
1	6602	SEE SHEET 1	10/19/01 BPP

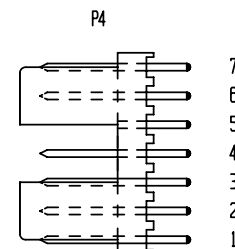


- REPLACE "J7" 0 OHM RESISTOR WITH A SURFACE MOUNT .5A 63V FUSE



NOTES:

1. REFER TO SCHEMATIC DIAGRAM AND PARTS LIST FOR REFERENCE DESIGNATIONS AND COMPONENT VALUES
2. MAXIMUM HEIGHT FOR ANY COMPONENT TO BE .450" EXCEPT FOR LED.
3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085".
4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815.
5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.
6. LEADS TO BE VERTICAL TO PCB DURING SOLDERING PROCESS (SEE DETAIL A).
7. CONNECTORS TO BE FLUSH TO PCB DURING SOLDERING PROCESS.
8. Q3-Q6 MUST BE FLUSH AND PERPENDICULAR TO BOARD.
9. CUT OFF TERMINAL PIN #5 ON P4 FLUSH TO HEADER.



SEE NOTE #9



COIN MECHANISMS INC.
400 REGENCY DR.
GLENDALE HGTS, IL

This drawing is the property of and contains proprietary information of Coin Mechanisms, Inc. The drawing shall be returned upon request and is not to be copied or disclosed without written authorization and will not be used in any way detrimental to the interest of Coin Mechanisms, Inc.

TITLE
PCB, CTRL, MC, 12V, 7MLX, INH-Y
SOC, COC, TOC, R12=3.9K

DRAWN	BPP	DATE	7/31/01
-------	-----	------	---------

CHKD		DATE	
------	--	------	--

APP		DATE	
-----	--	------	--

DWG NO.	0927-000530	REV
---------	-------------	-----

Y	01/01/2022	SHT 2
---	------------	-------

SIZEB	SCALE X=X	g:elec dig re	927.200530	SHT 2 OF 3
-------	-----------	---------------	------------	------------