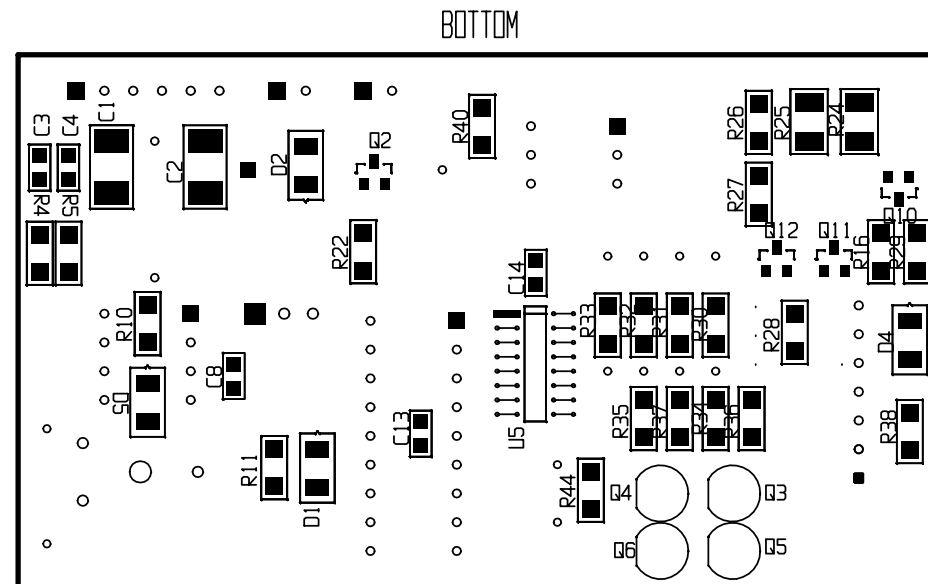
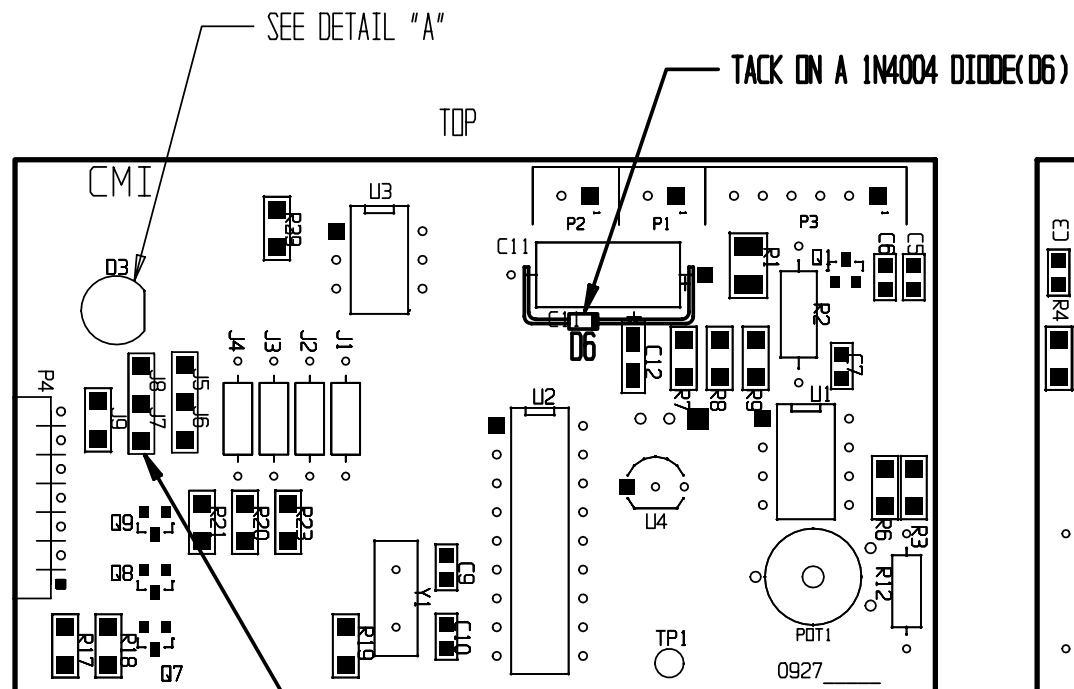
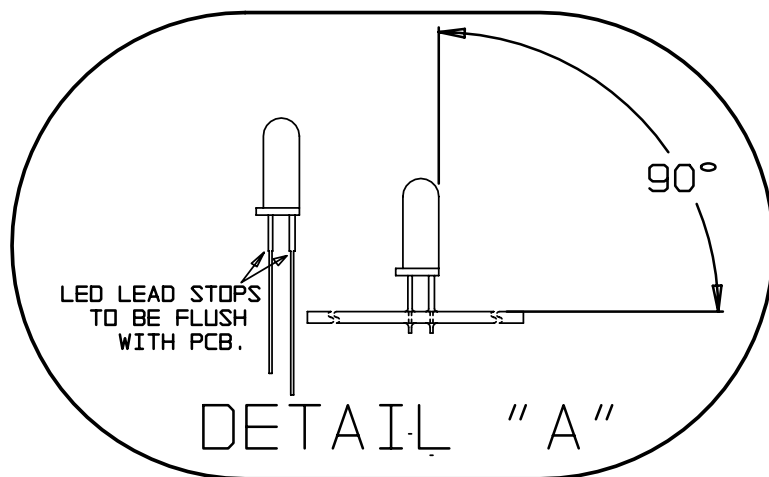


NO.	ECN NO.	REVISIONS	DATE/BY
1	0601 0602	SEE SHEET 1	10/19/01 BPP

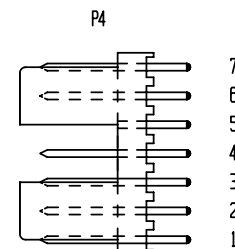


- REPLACE "J7" 0 OHM RESISTOR WITH A SURFACE MOUNT .5A 63V FUSE



- NOTES:

1. REFER TO SCHEMATIC DIAGRAM AND PARTS LIST FOR REFERENCE DESIGNATIONS AND COMPONENT VALUES
2. MAXIMUM HEIGHT FOR ANY COMPONENT TO BE .450" EXCEPT FOR LED.
3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085".
4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815.
5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.
6. LEADS TO BE VERTICAL TO PCB DURING SOLDERING PROCESS (SEE DETAIL A).
7. CONNECTORS TO BE FLUSH TO PCB DURING SOLDERING PROCESS.
8. Q3-Q6 MUST BE FLUSH AND PERPENDICULAR TO BOARD.
9. CUT OFF TERMINAL PIN #5 ON P4 FLUSH TO HEADER.



SEE NOTE #9



COIN MECHANISMS INC.
400 REGENCY DR.
GLENDALE HGTS, IL

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TITLE
PCB, CTRL, MC, 12V, 7MLX, INH-Y
SOC, COC, TOC, R12=1K

DRAWN	BPP	DATE	7/31/01
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CHKD		DATE	
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APP		DATE	
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DWG NO.	0927-000531	REV
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Y:\a\elc\doe-re\027 200531	SHT 2 PE
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SIZEB	SCALE X=X	g:elec dig re	027.200530	SHT 2 OF 3
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